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IMPLEMENTATION OF FFT ALGORITHM

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ABSTRACT

Fast Fourier Transform (FFT) is a fast and efficient way of computing Discrete Fourier Transform (DFT). FFT is one of the exquisite and ubiquitous operations in the field of digital signal processing. Moreover, it is one of the critical components in Orthogonal Frequency Division Multiplexing (OFDM) [5] systems. FFT can be of two types, namely: Decimation in time (DIT) FFT and Decimation in frequency (DIF) FFT. For most of the real life situations like audio/image/video processing etc., DIT-FFT has an advantage over DIF-FFT since it does not require any output recording. In this paper, an efficient algorithm to compute 8 point FFT has been devised in which a butterfly unit (stage-I) computes the output and then feeds those outputs as inputs to the next butterfly units (stage-II/III) so as to compute the overall FFT.

KEYWORDS: FFT, DFT, Twiddle factor, Butterfly unit, Bit reversal.

INTRODUCTION

FFT is very popular for transforming a signal from time domain to frequency domain and it has quite an interesting history starting from 1805, when Carl Fredrich Gauss tried to determine the orbits of various asteroids from sample locations. He thereby developed the DFT algorithm even before Fourier published his results for the same in 1822. He developed an algorithm similar to that of Cooley [4] and Tukey [4] but Gauss never published his method or algorithm in his lifetime. It took another 160 years until Cooley and Tukey reinvented the FFT. During this period of 160 years from 1805 to 1965, many other scientists invented various efficient algorithms to compute FFT but none of them was as general as that of Gauss's algorithm.

The results of FFT are same as that of DFT; the only difference is that the algorithm is optimised to remove the redundant calculations, the FFT greatly reduces the amount of calculations required and hence reduces the time required for the computation. Functionally, the FFT decomposes the set of data to be transformed into a series of smaller data sets to be transformed. Then it decomposes those smaller sets into even smaller sets. At each stage of processing, the results of the previous stage are combined in a special way. Finally, it calculates the DFT of each small data set. DFT can be computed using the below given formula:

$$X[k] = \sum_{k=0}^{N-1} x_k e^{-\left(\frac{2\pi j}{N}\right)nk} ; \quad k = 0, 1, \dots, N - 1.$$

A major drawback of this DFT algorithm is the computational complexity. For a sequence of length N, it has a complexity given as: $O(N^2)$ hence it is not a very efficient method and here the FFT comes into the picture. FFT significantly reduces the number of computations required for a sequence of length N from $O(N^2)$ to $O(N \log N)$ where log is the base-2 logarithm. FFT operates by decomposing an N point time domain signal into N time domain signals each composed of a single point. The second step is to calculate the N frequency spectra corresponding to these N time domain signals. Lastly, the N spectra are synthesized into a single frequency

spectrum. So we split the N-point data sequence into two N/2 point data sequences $f_1(n)$ and $f_2(n)$, corresponding to the even-numbered and odd-numbered samples of $x(n)$ respectively.

$$f_1(n) = x(2n)$$

$$f_2(n) = x(2n + 1); \quad n = 0, 1, 2, \dots, \frac{N}{2} - 1.$$

Here $f_1(n)$ and $f_2(n)$ are obtained by decimating $x(n)$ by a factor of 2, and hence the resulting FFT is called a decimation-in-time (DIT) algorithm. Now the N-point DFT can be expressed as:

$$\begin{aligned} X(k) &= \sum_{n=0}^{N-1} x(n) W_N^{kn}; \quad k = 0, 1, 2, \dots, N - 1. \\ &= \sum_{n \text{ even}}^{N-1} x(n) W_N^{kn} + \sum_{n \text{ odd}}^{N-1} x(n) W_N^{kn} \\ &= \sum_{m=0}^{\frac{N}{2}-1} x(2m) W_N^{2mk} + \sum_{m=0}^{\frac{N}{2}-1} x(2m + 1) W_N^{(2m+1)k} \end{aligned}$$

IMPLEMENTATION OF FFT

To compute FFT, we need to break a data set into smaller data sets and then compute the DFT of each small set. Figure 1 describes the implementation of 8-point DFT. We observe that computation is performed in three stages, starting with the computation of four 2-point DFTs, then two 4-point DFTs, and finally one 8-point DFT.

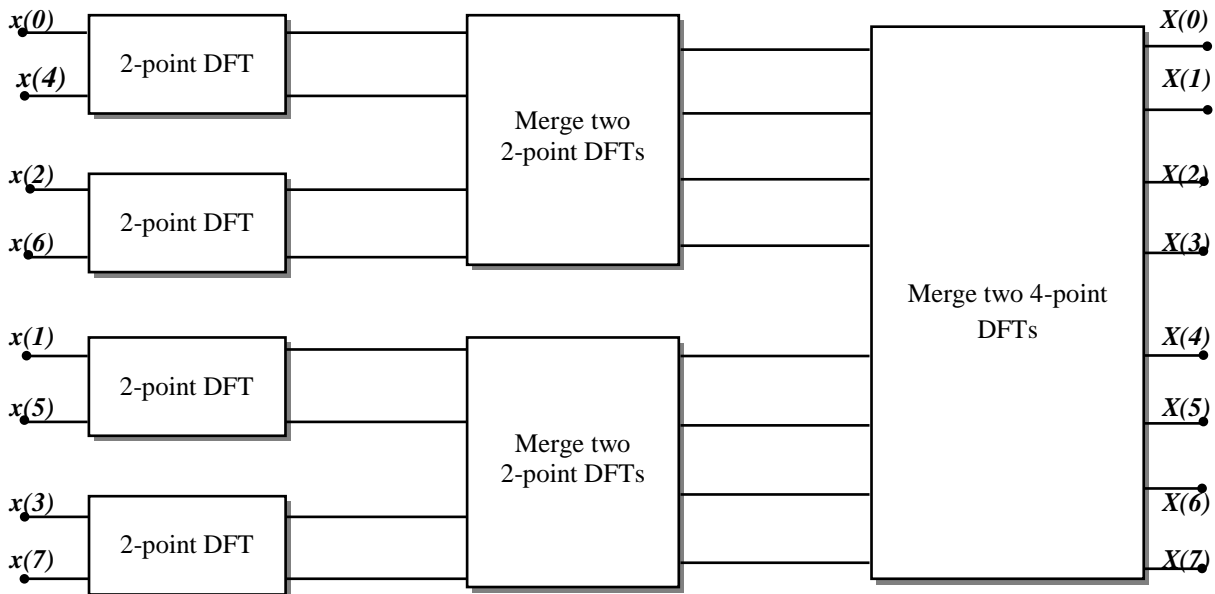


Fig 1: FFT block diagram

BUTTERFLY UNIT [7]

In radix-2 Cooley-Tukey algorithm, butterfly is simply a 2-point DFT that takes two inputs and gives two outputs. Butterfly unit is the basic building block for FFT computation. The figure 2 shown below describes the basic butterfly unit used in FFT implementation.

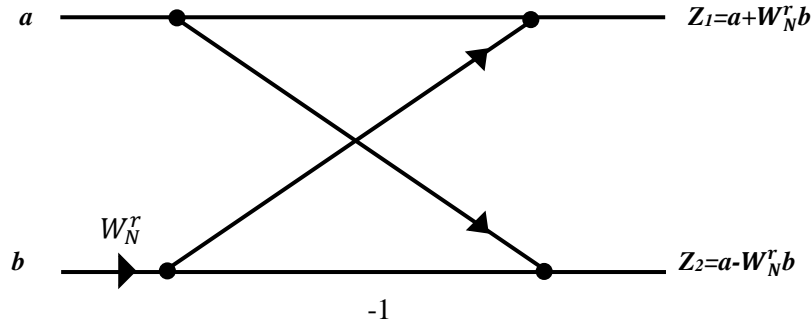


Fig 2: Basic butterfly unit

Implementation of FFT requires the computation of butterfly unit at first, which takes two complex inputs ‘a’ and ‘b’ and a twiddle factor ‘W’. It generates two complex outputs ‘Z₁’ and ‘Z₂’.

- Input ‘b’ is multiplied with ‘W’ and then added to ‘a’ for the output ‘Z₁’.
- Input ‘b’ is multiplied with ‘-1’ and then added to ‘a’ for the output ‘Z₂’.

This basic butterfly unit is replicated four times each in the three stages of the FFT and the final output is generated as shown in the figure 3 below:

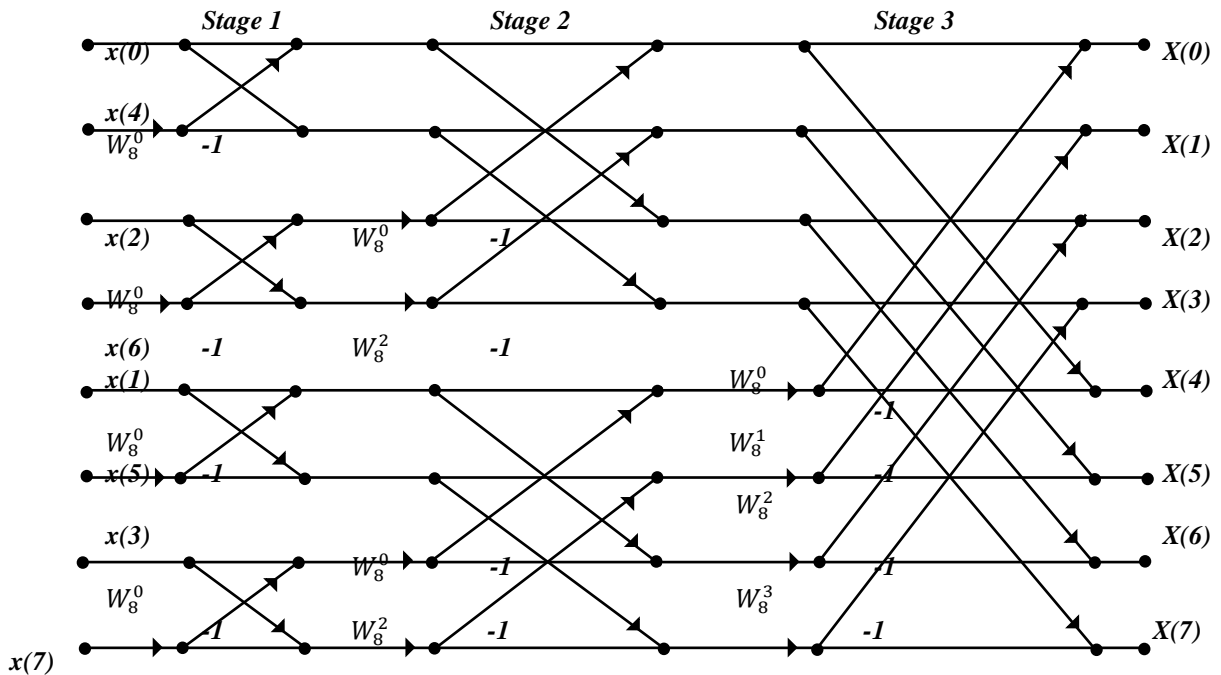


Fig 3: FFT butterfly diagram

BIT REVERSAL ^[8]

In FFT computation, the input bits need to be bit reversed so as to obtain the output bits in the normal order. To achieve this, the input sequence is broken into even and odd parts based on their indexes. Then these even parts are again broken down into even and odd parts. Same is done with odd parts respectively. This procedure is continued until only two points are left. In this paper, the sequence is separated in time domain. For example, if we consider the case where N=8, input sequence is x(0), x(1), x(2), x(3), x(4), x(5), x(6), x(7) and after the bit reversal process, the output sequence obtained is x(0), x(4), x(2), x(6), x(1), x(5), x(3), x(7) as shown in table 1 below:

Table 1: Bit reversal

Input	Address (binary)	Output (bit reversed)	Address (binary)
x(0)	000	x(0)	000
x(1)	001	x(4)	100
x(2)	010	x(2)	010
x(3)	011	x(6)	110
x(4)	100	x(1)	001
x(5)	101	x(5)	101
x(6)	110	x(3)	011
x(7)	111	x(7)	111

RESULTS

Result of FFT for the following input has been verified on MATLAB:

$$x(n) = (1, 2, 4, 8, 16, 32, 64, 128);$$

and the output was recorded as:

$$y(n) = (255, 48+166i, -51+102i, -78+46i, -85, -78-46i, -51-102i, 48-166i);$$

The waveform of input sequence is shown in figure 5 below and the corresponding output sequence waveform is shown in figure 6 below. Figure 4 displays the verified output on MATLAB software.

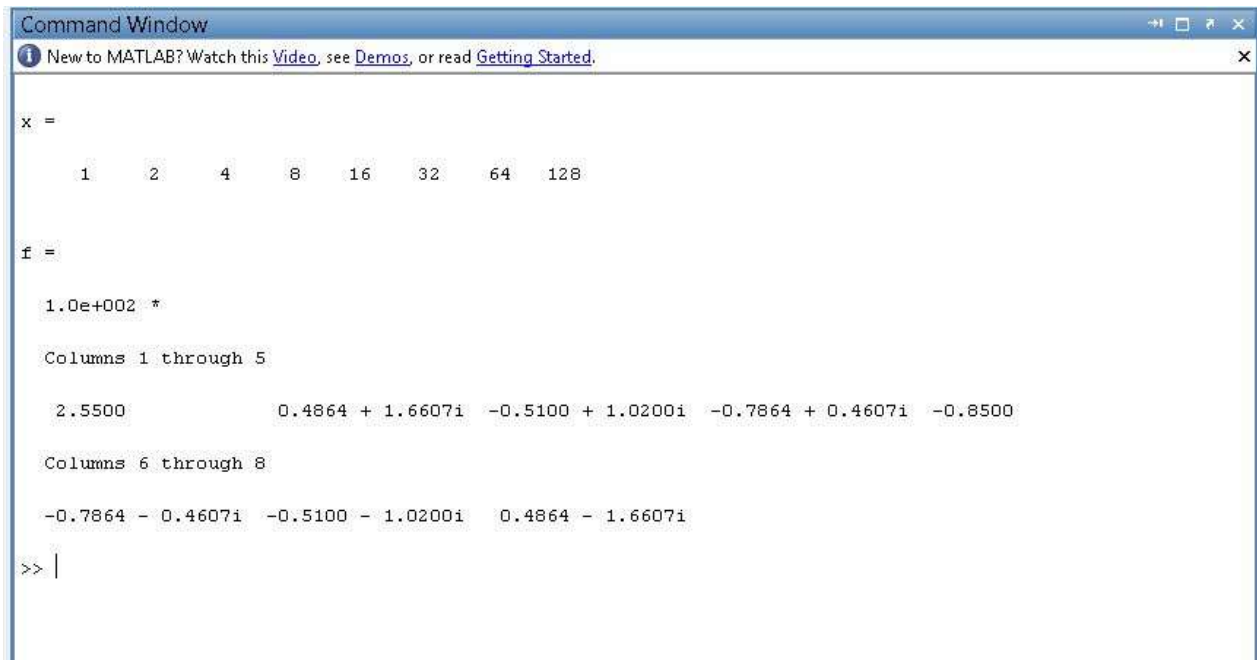


Fig 4: Output verification on MATLAB ^[9]

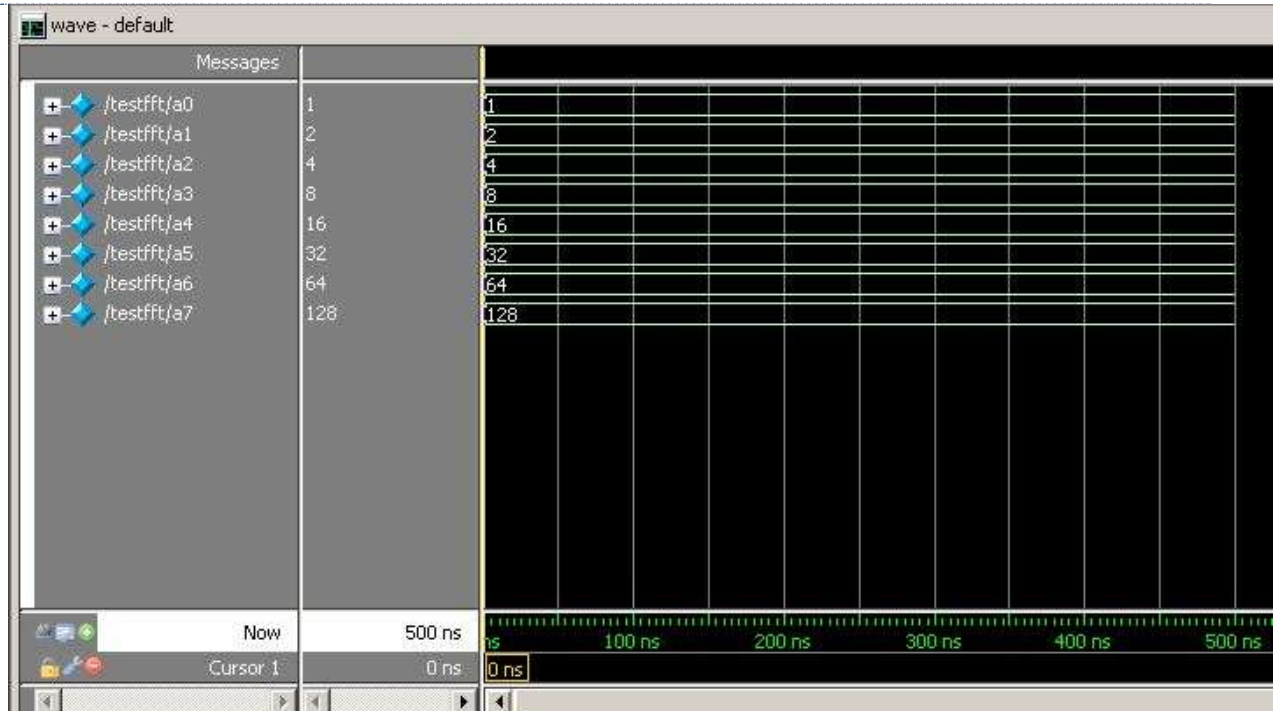


Fig 5: Input sequence

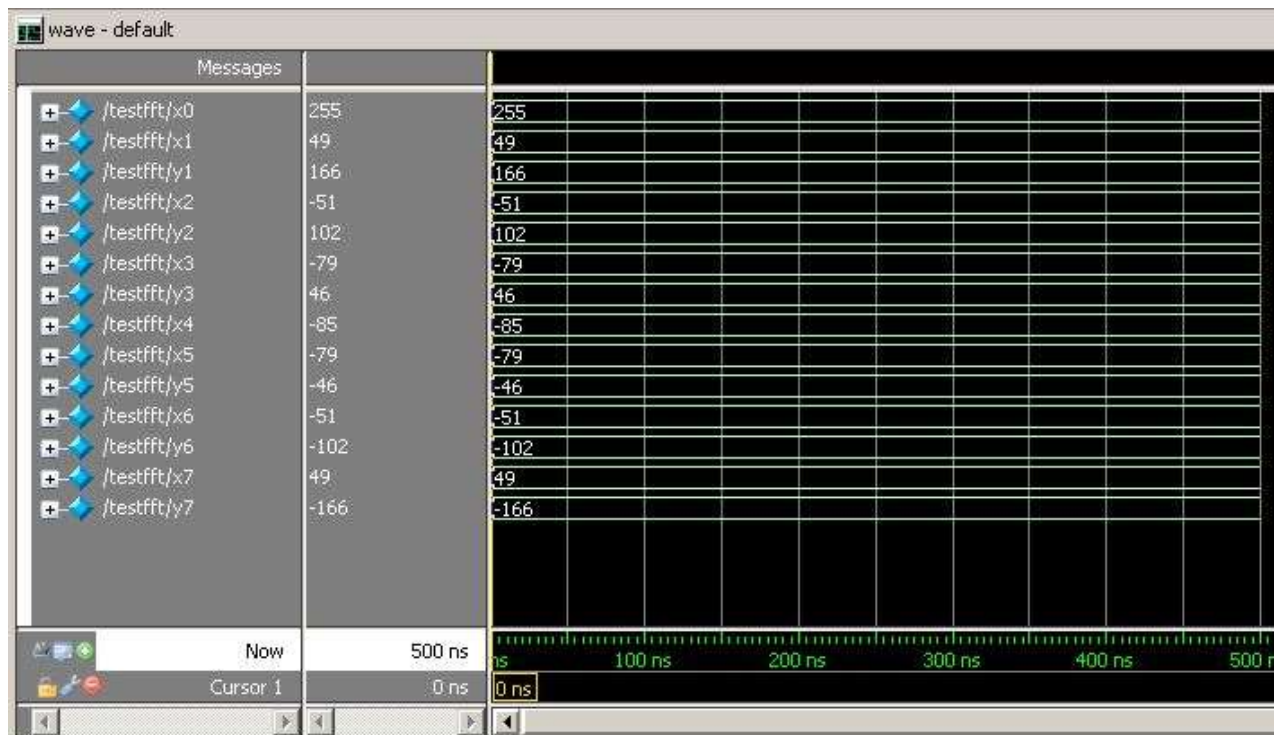


Fig 6: Output sequence



CONCLUSION

The Fast Fourier Transform (FFT) is simply a professional method to compute the Discrete Fourier Transform (DFT). Use of FFT reduces the complexity and the time required for the computation of DFT. A verilog implementation of floating point FFT with bit reversal has been generated using single precision floating point number IEEE 754 standard.

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